

Fig. 2

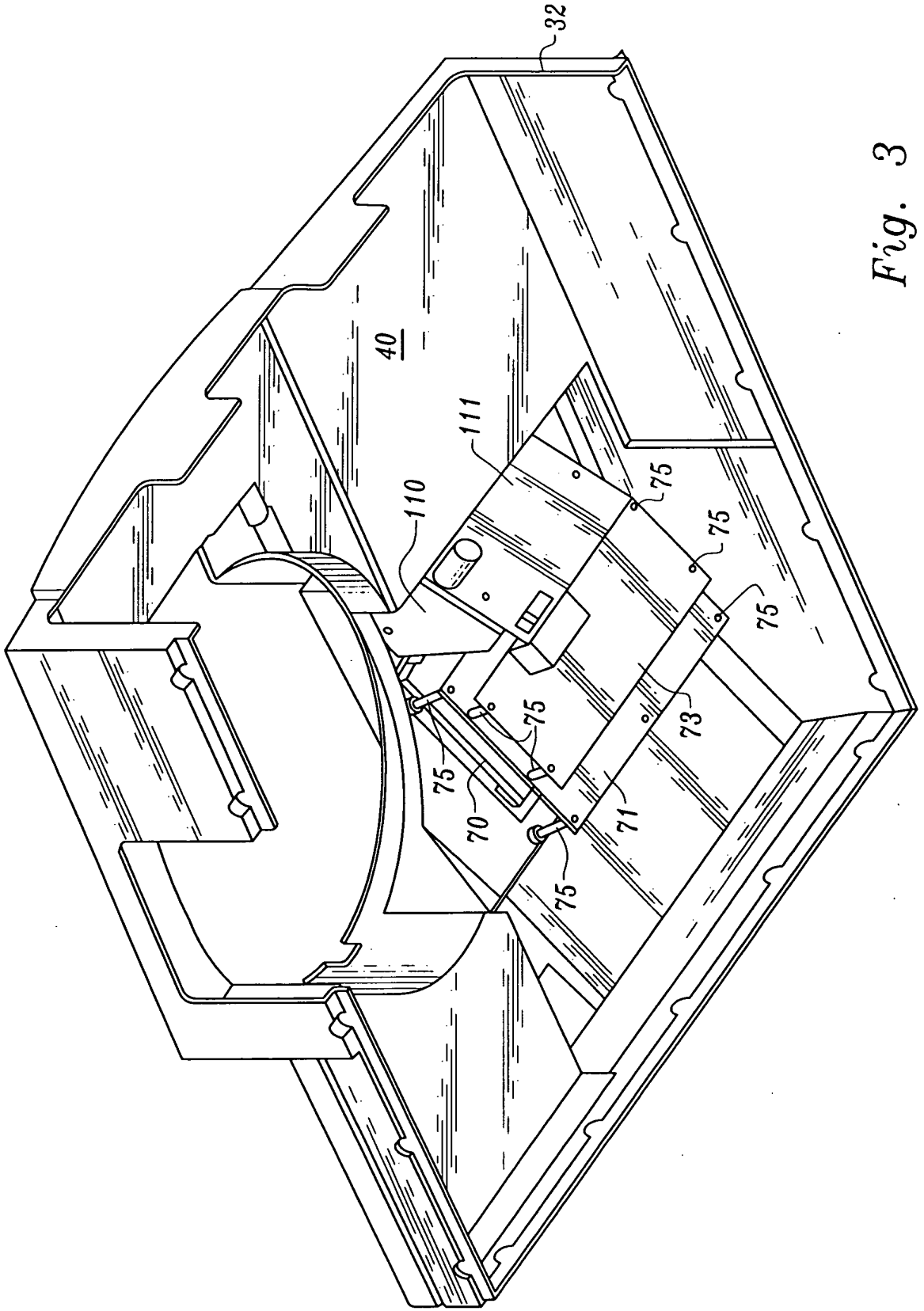


Fig. 3

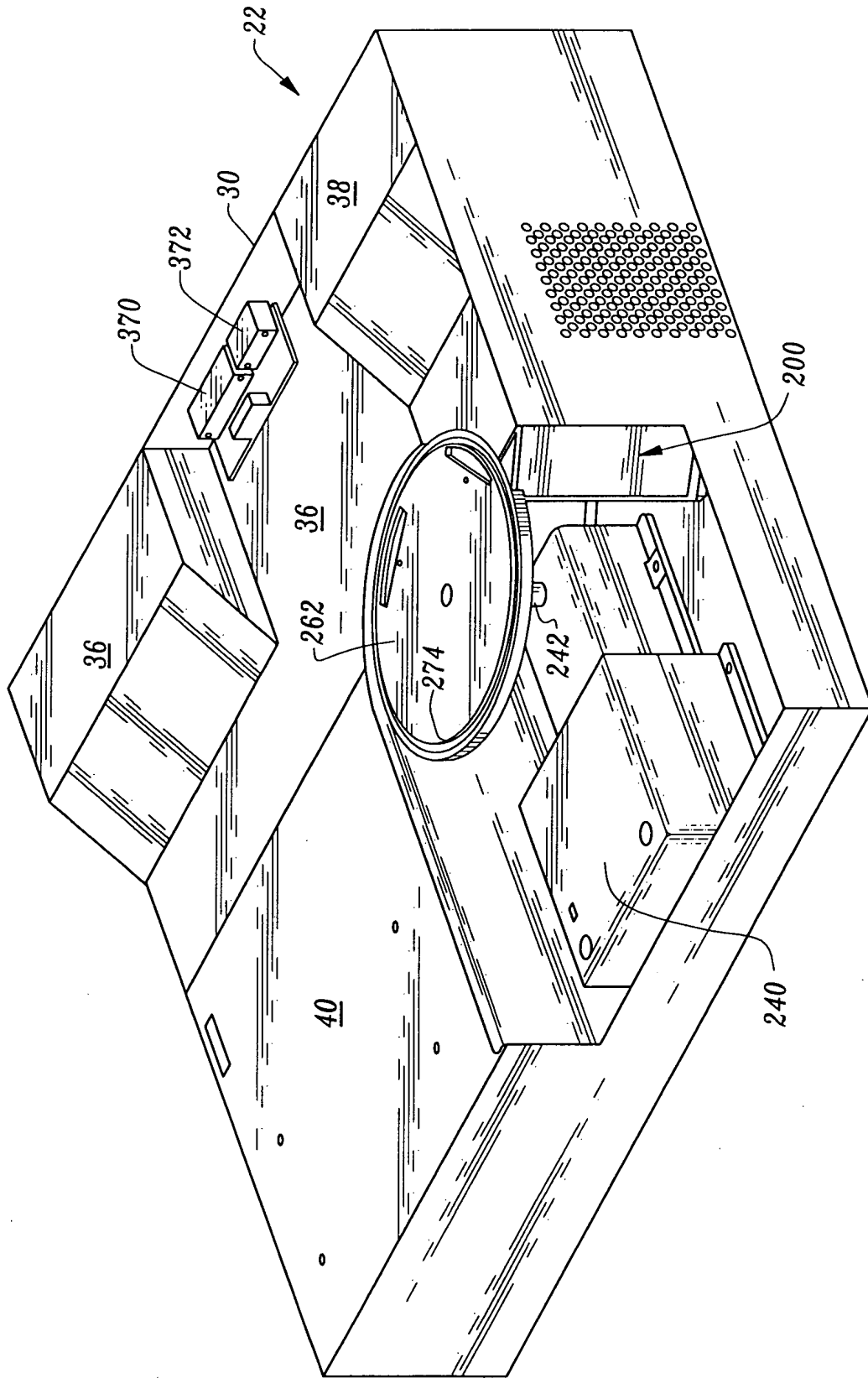


Fig. 4

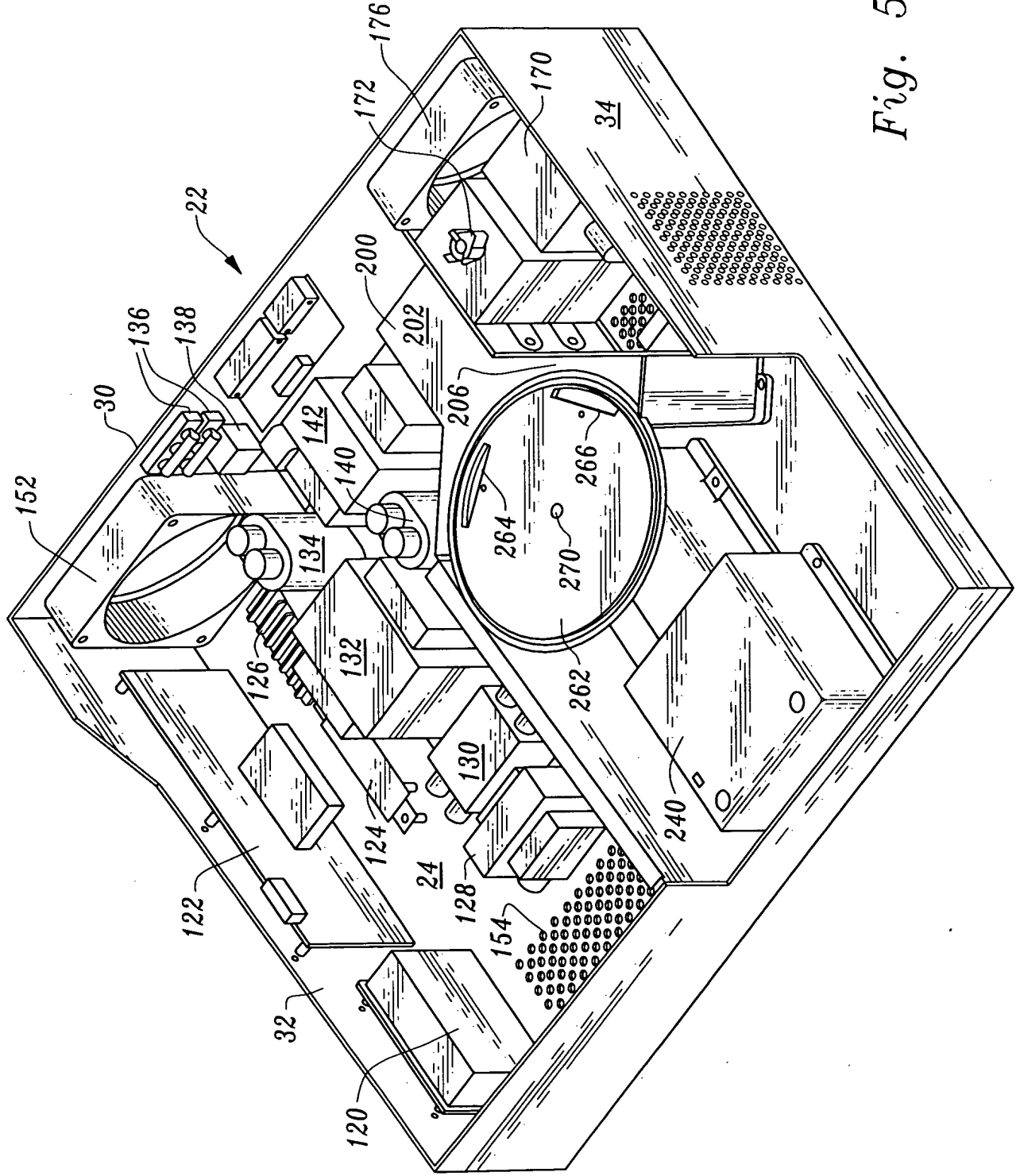
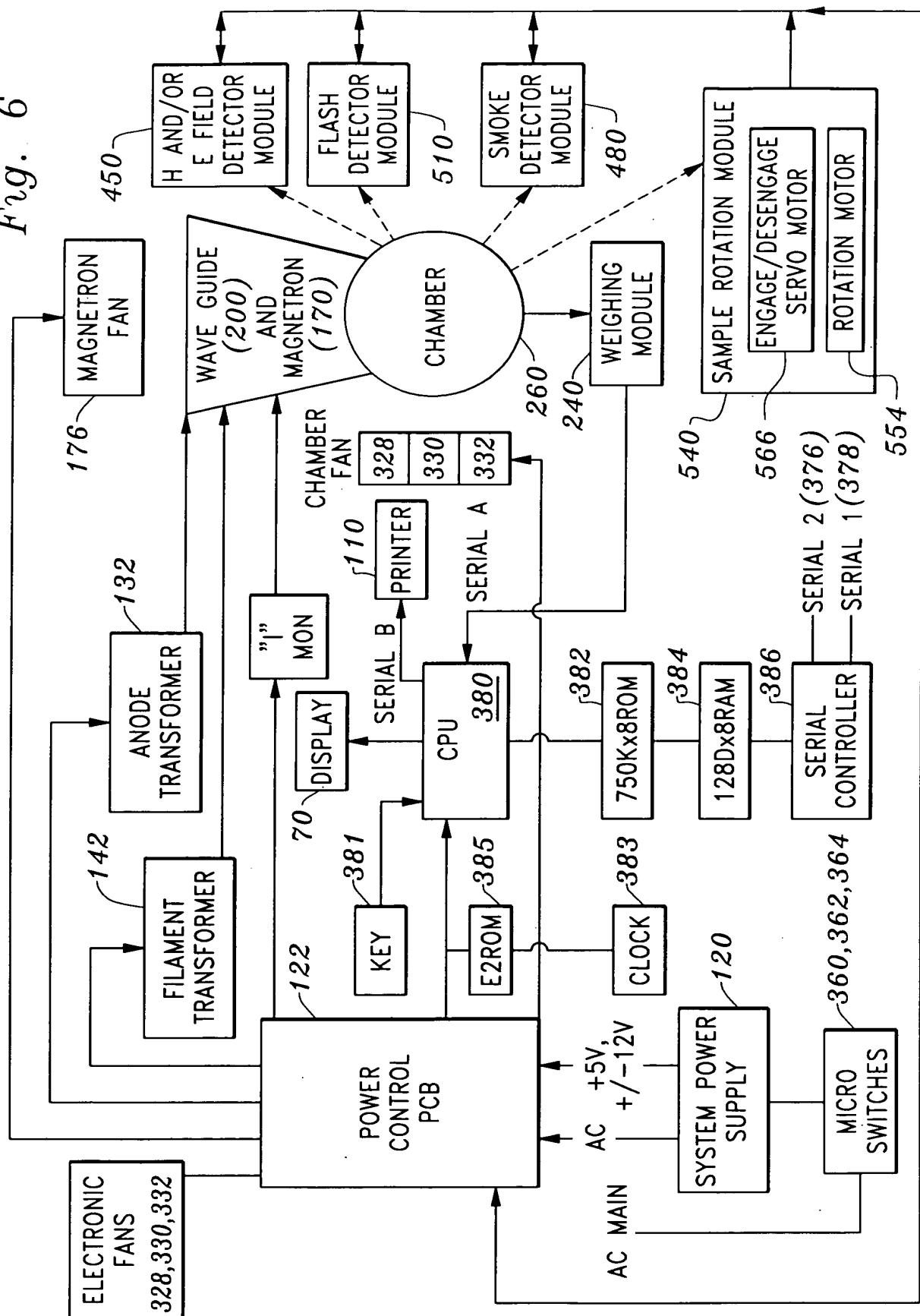


Fig. 5

Fig. 6



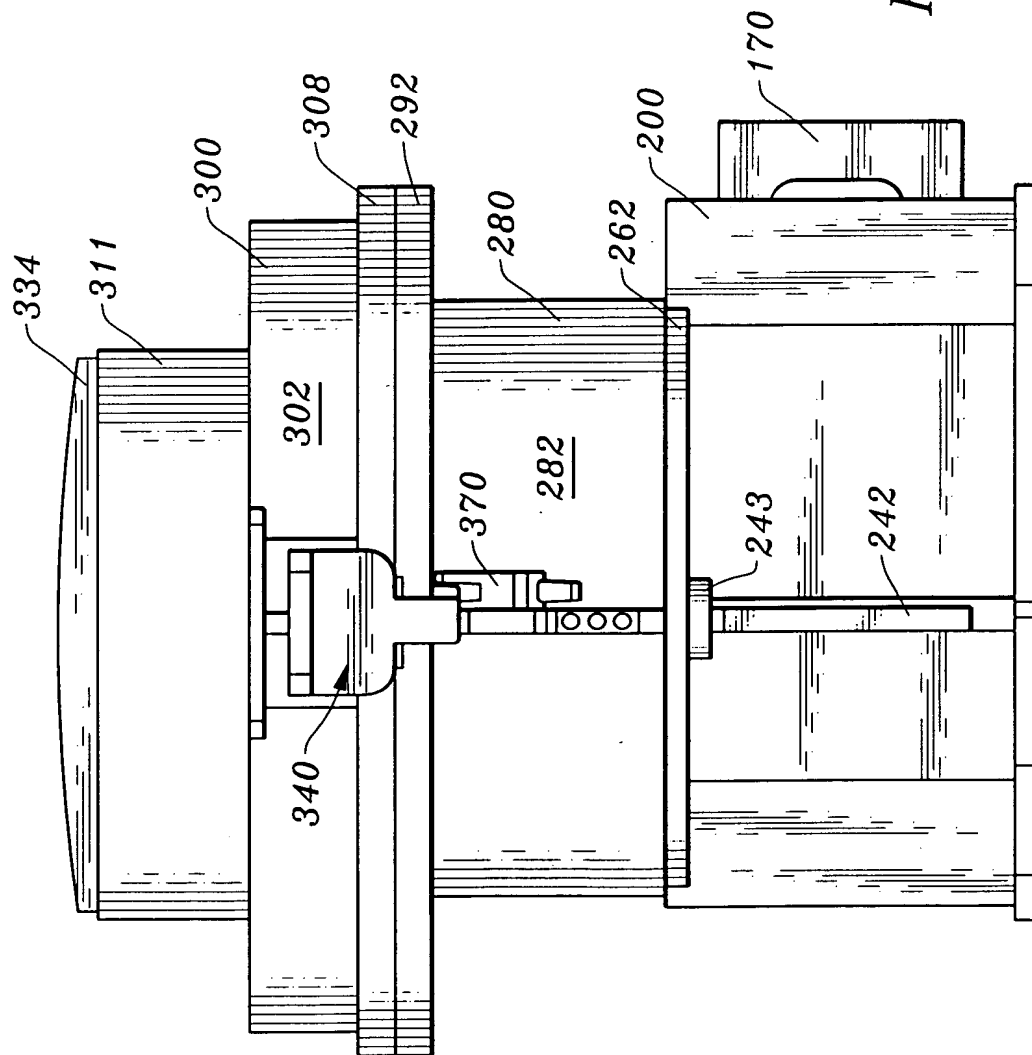
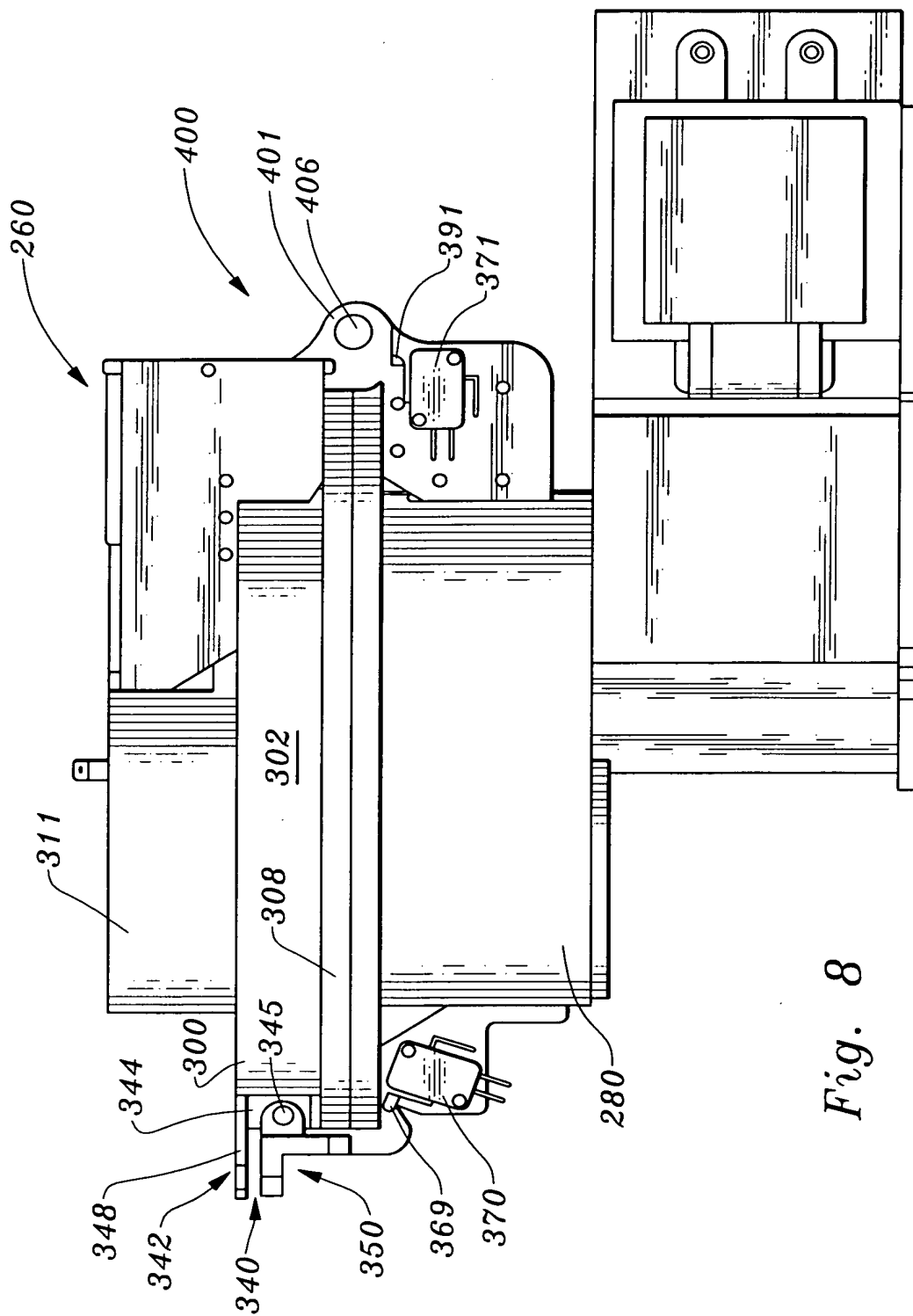


Fig. 7





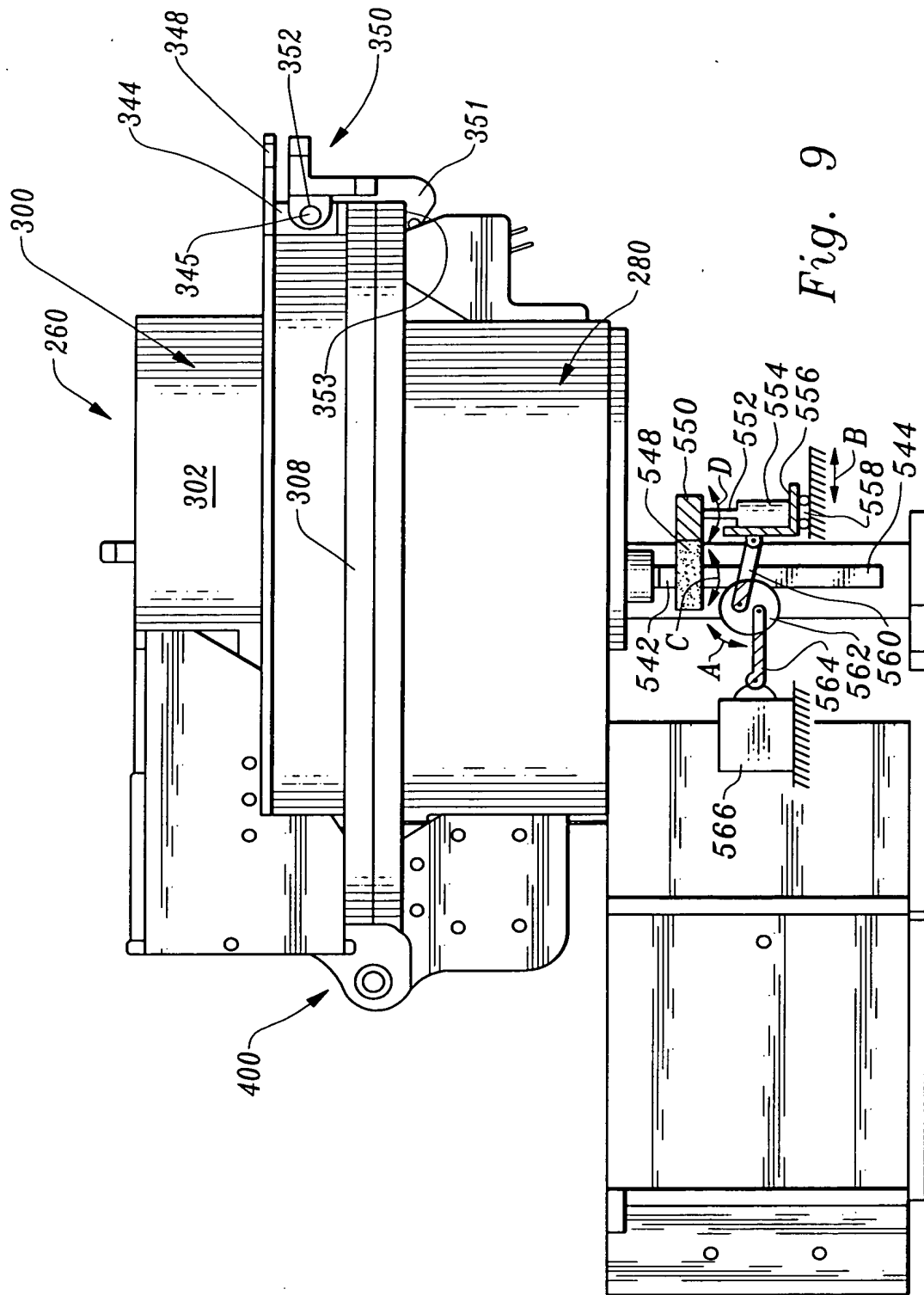


Fig. 9

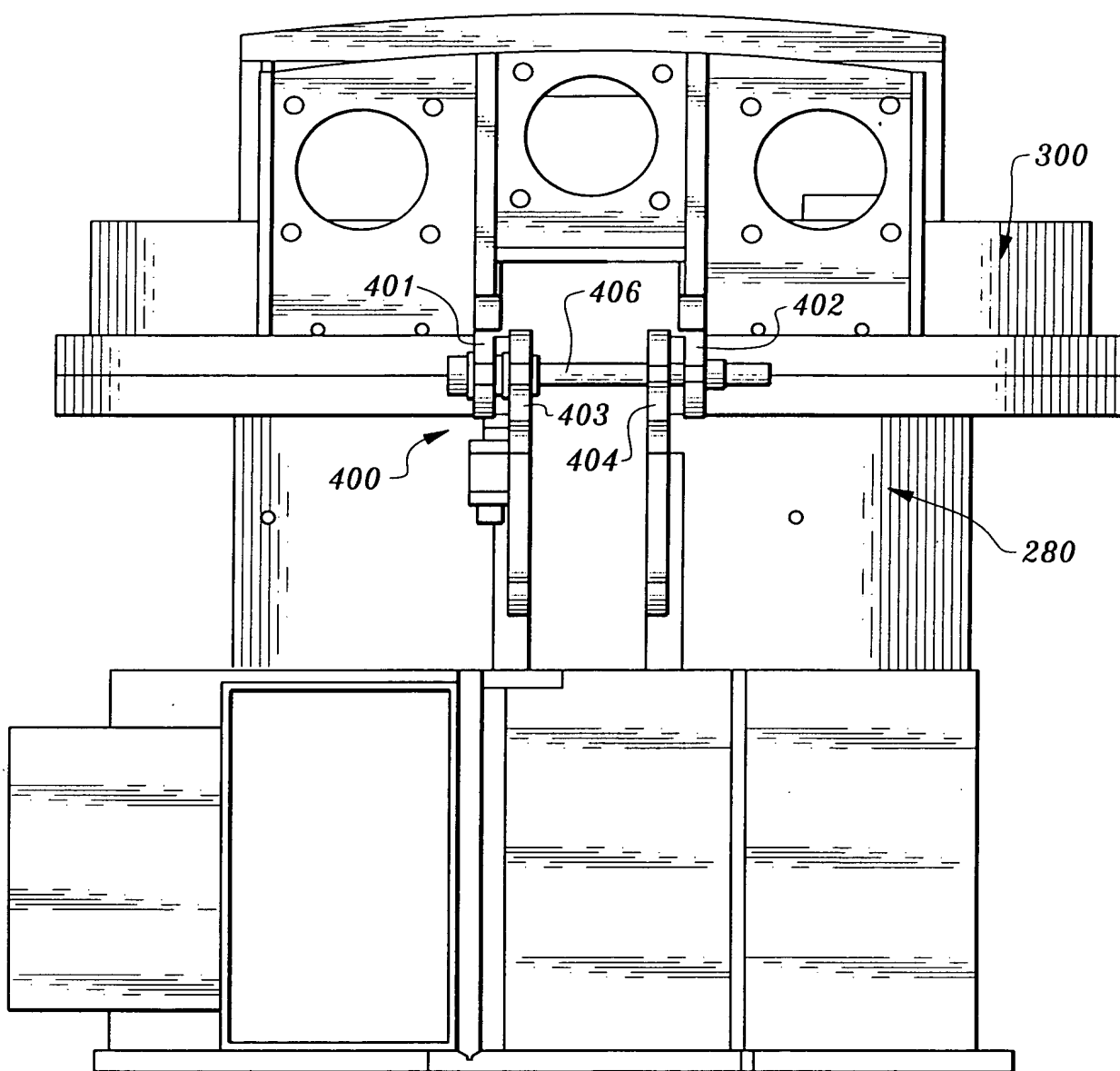


Fig. 10

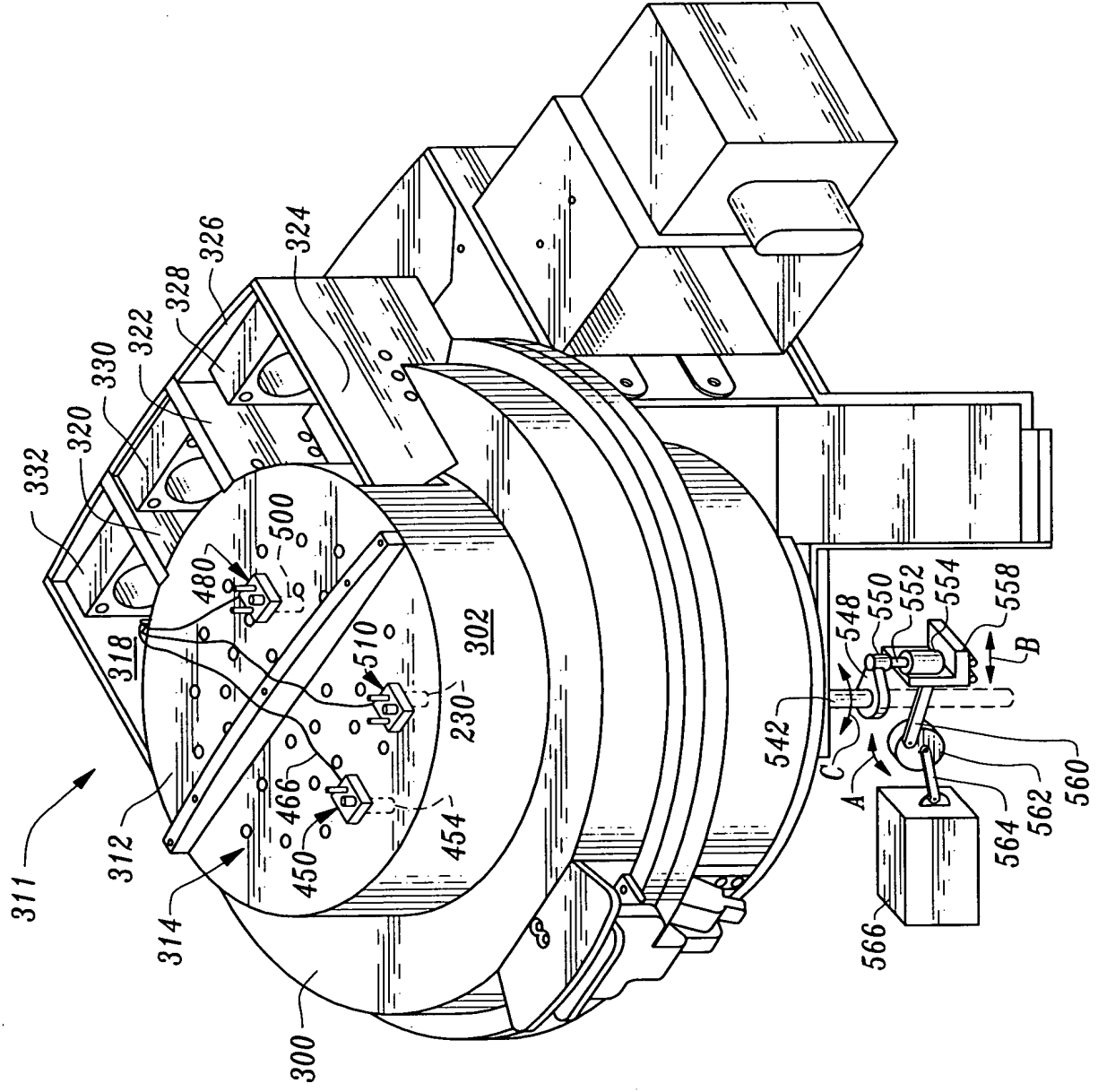


Fig. 11

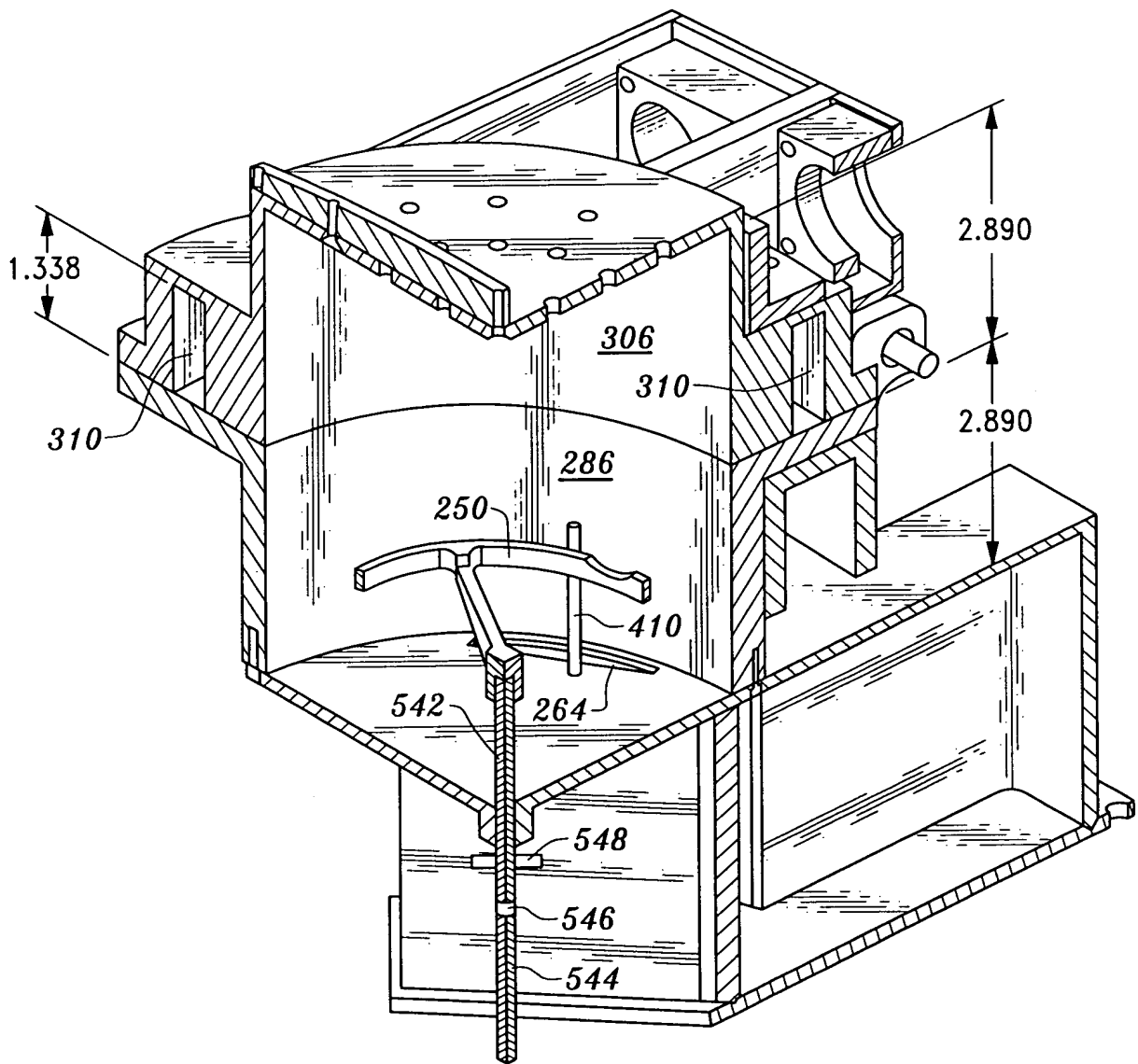


Fig. 12

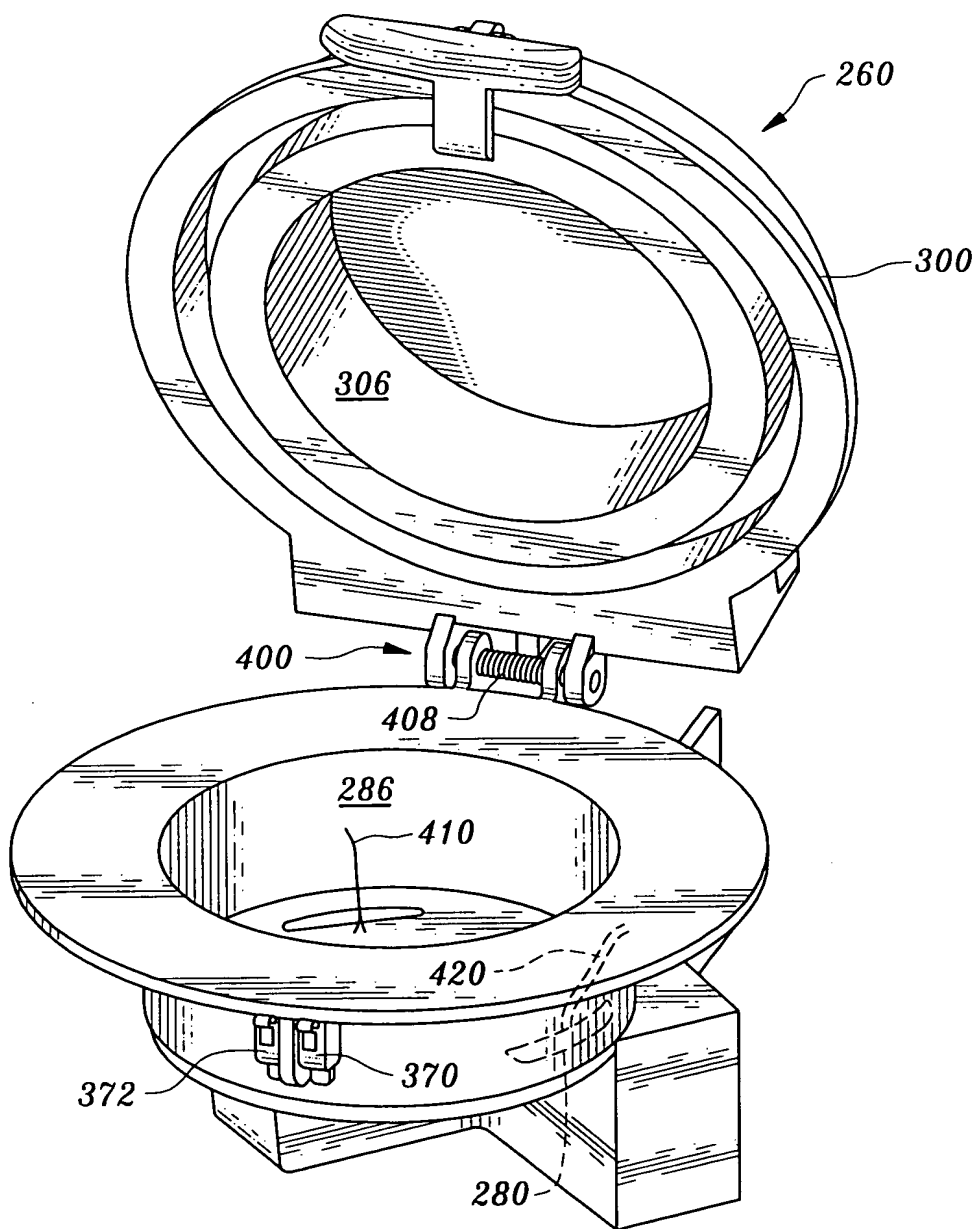


Fig. 13

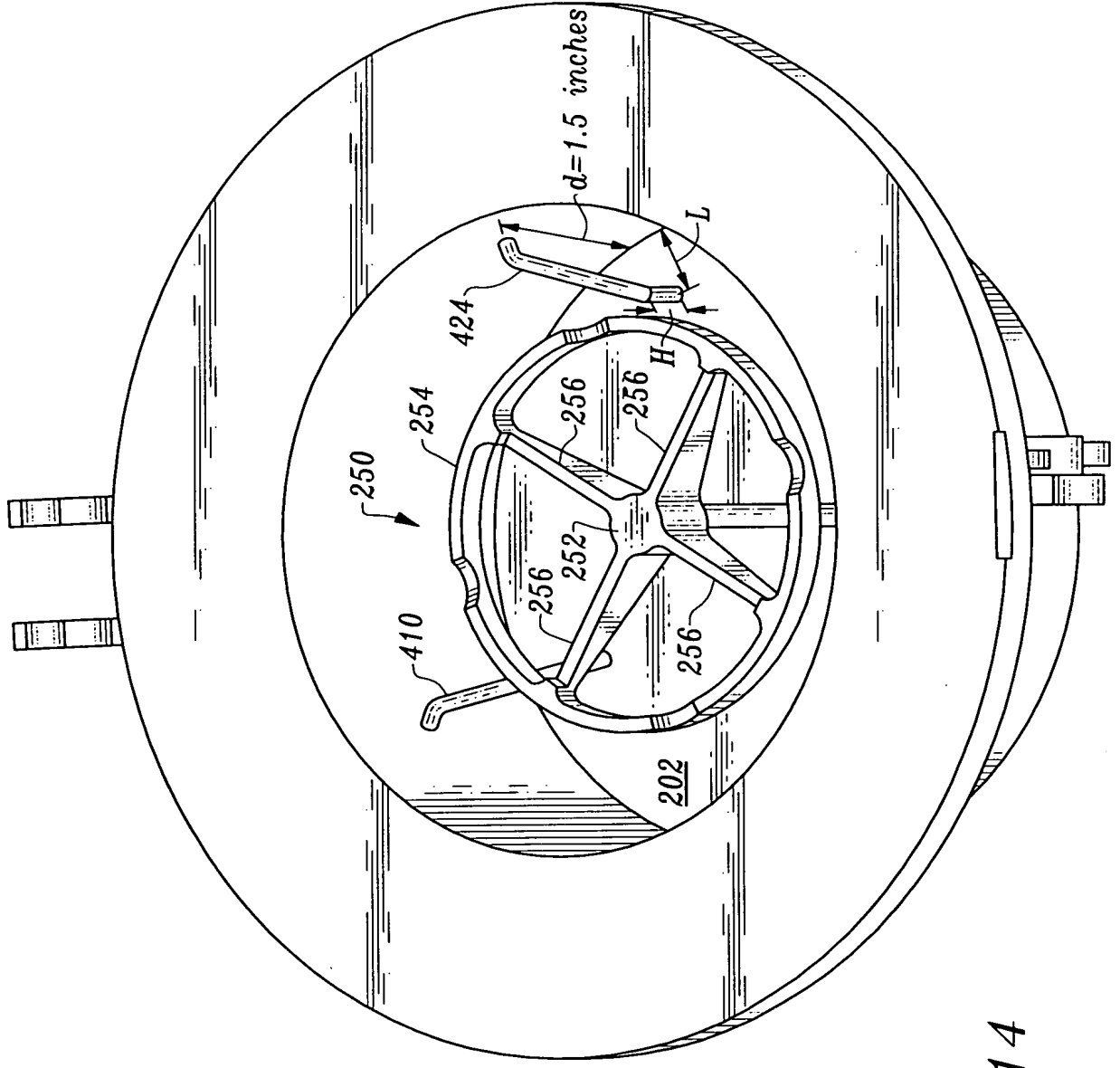


Fig. 14

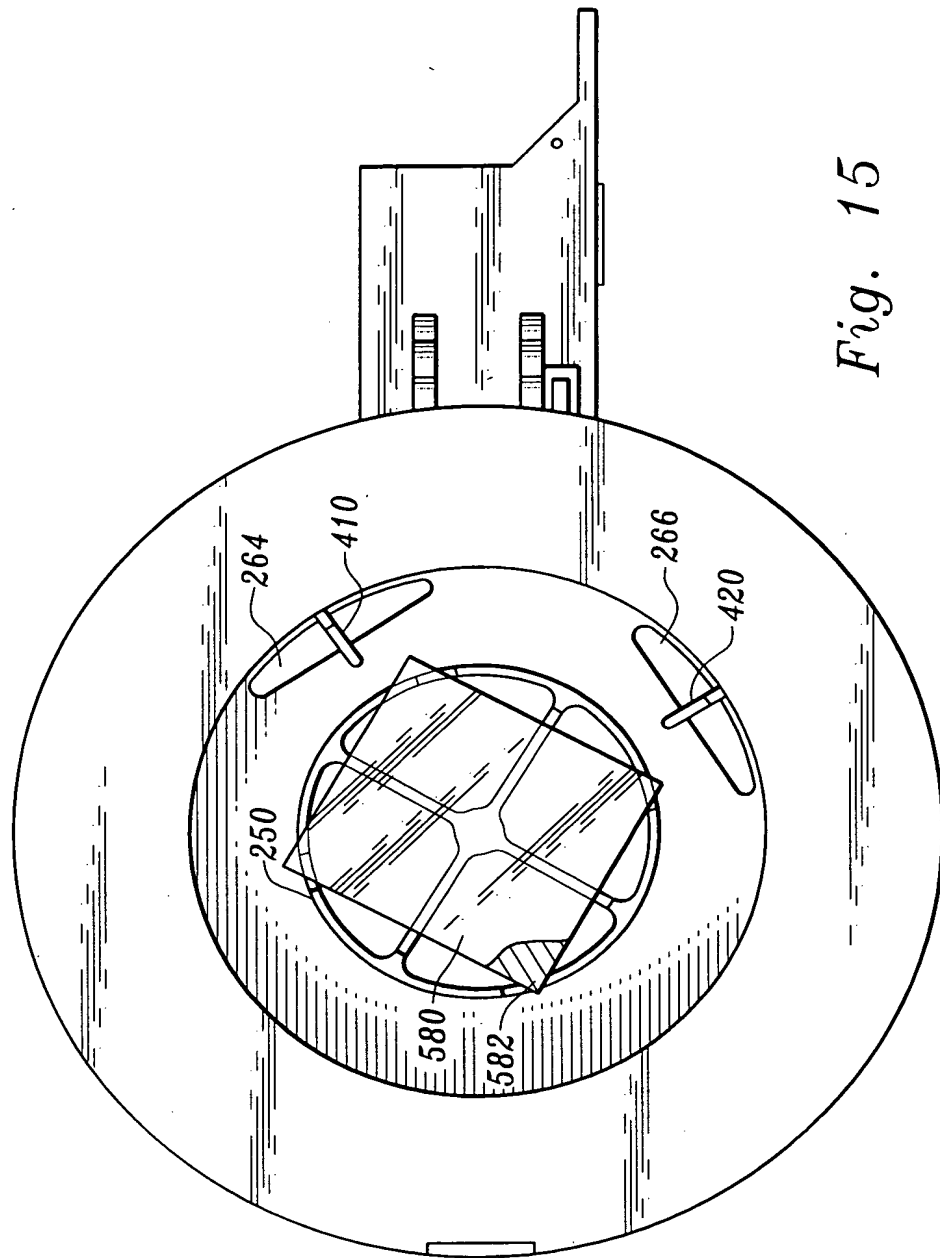


Fig. 15

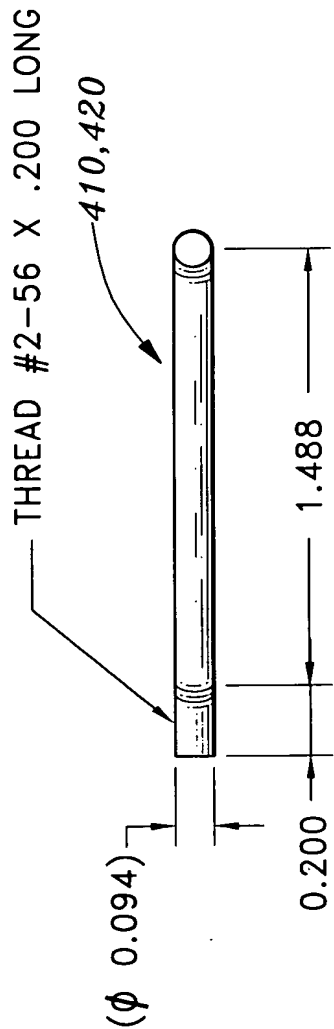


Fig. 16a

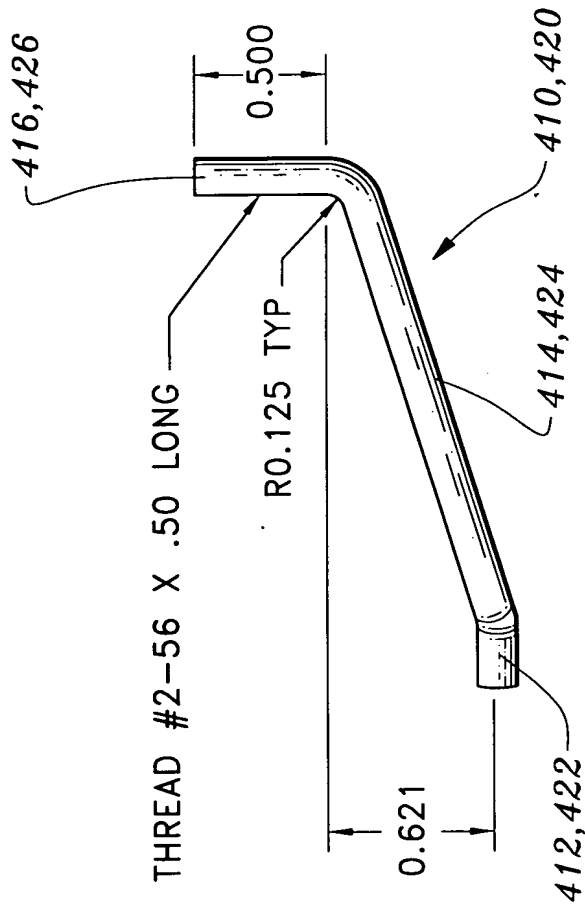


Fig. 16b



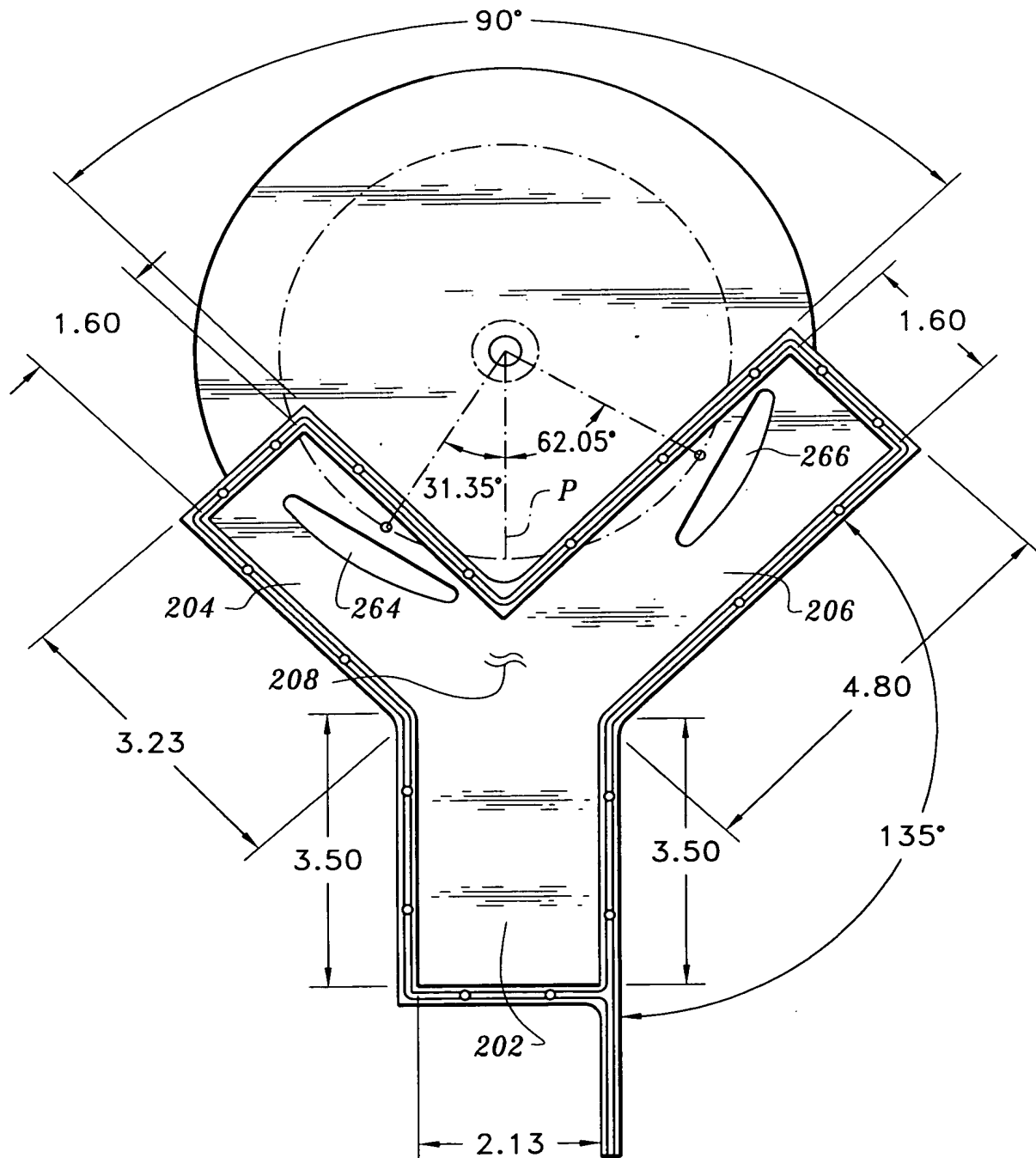


Fig. 17

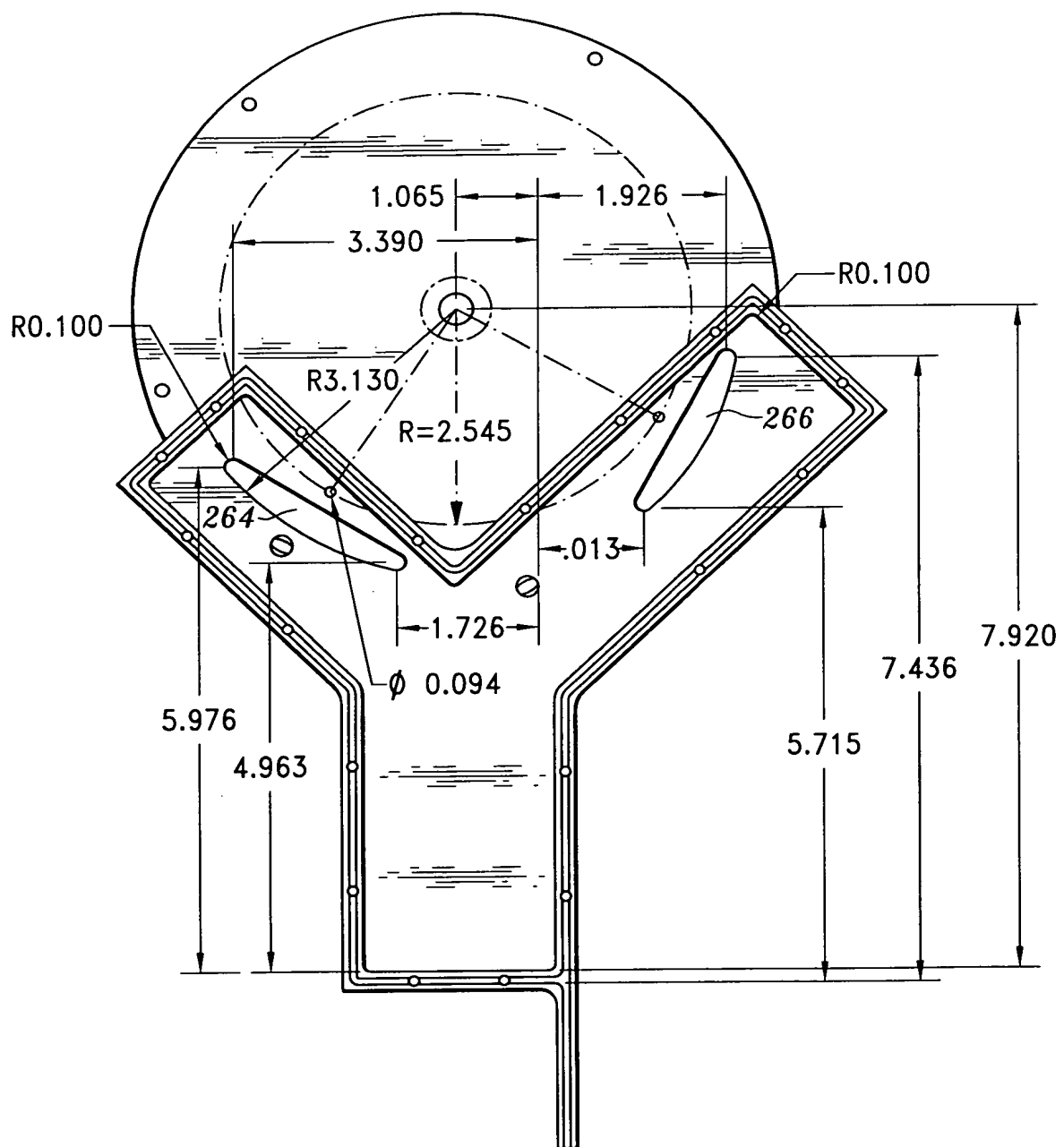


Fig. 18

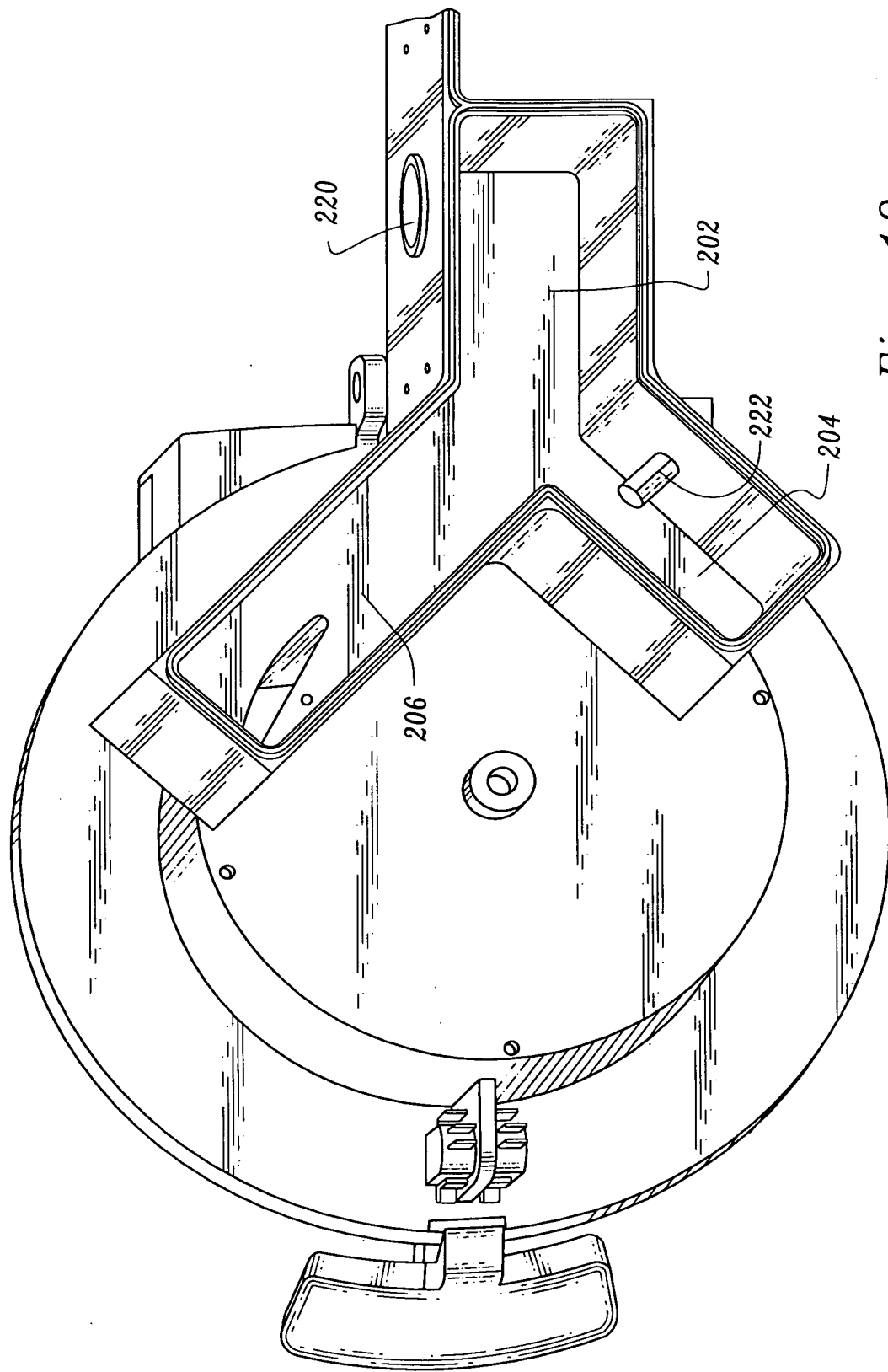


Fig. 19



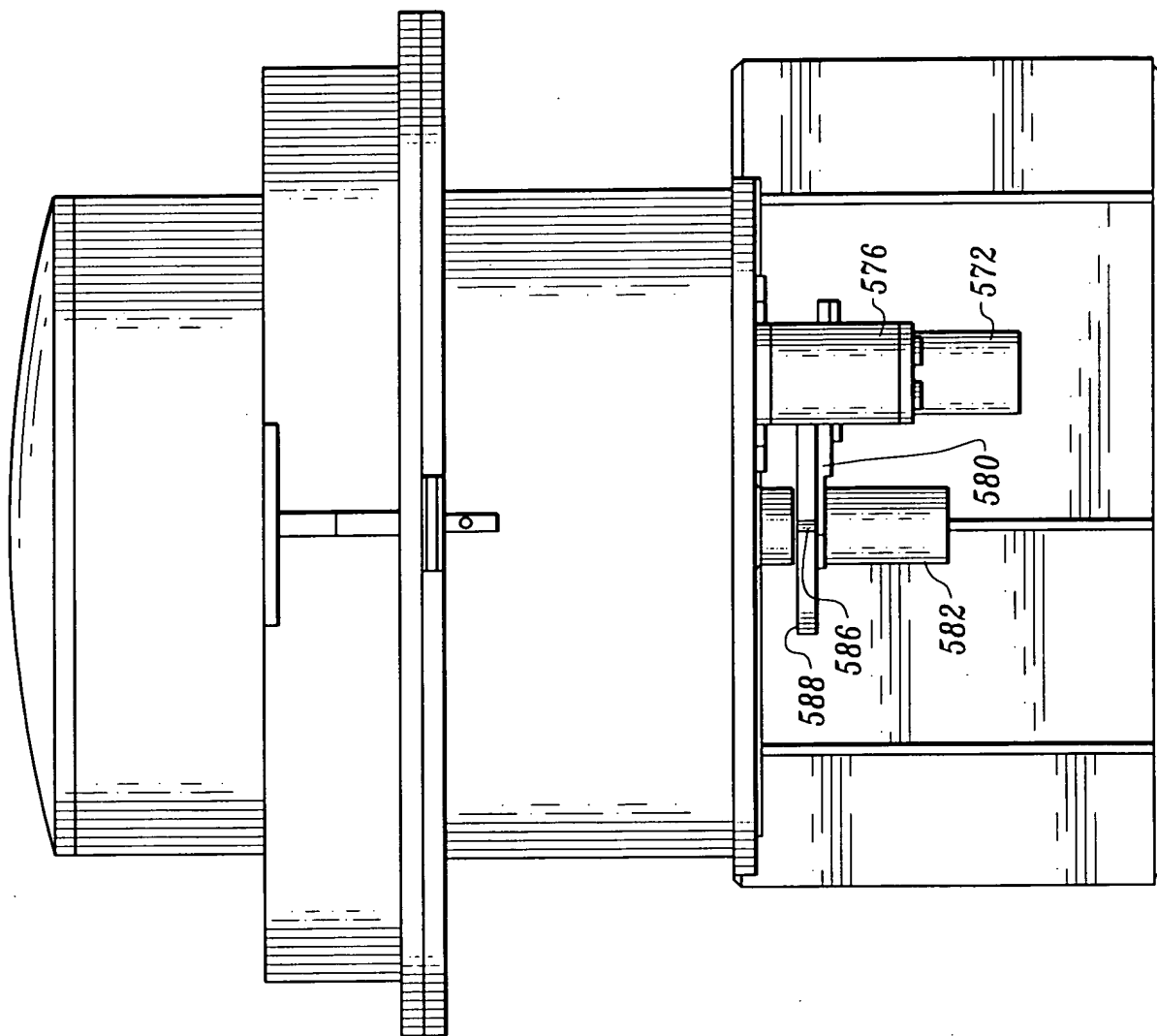


Fig. 20B

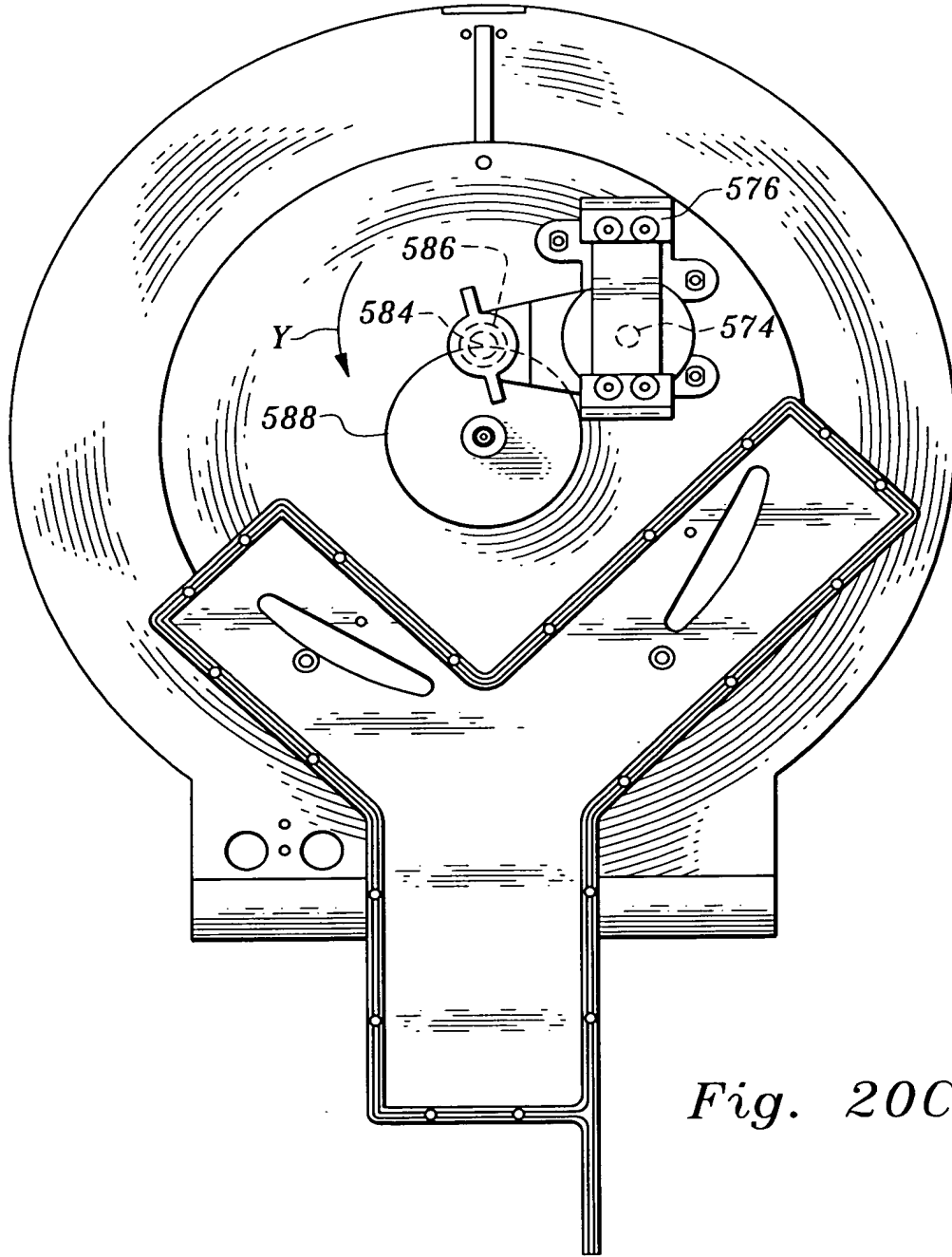
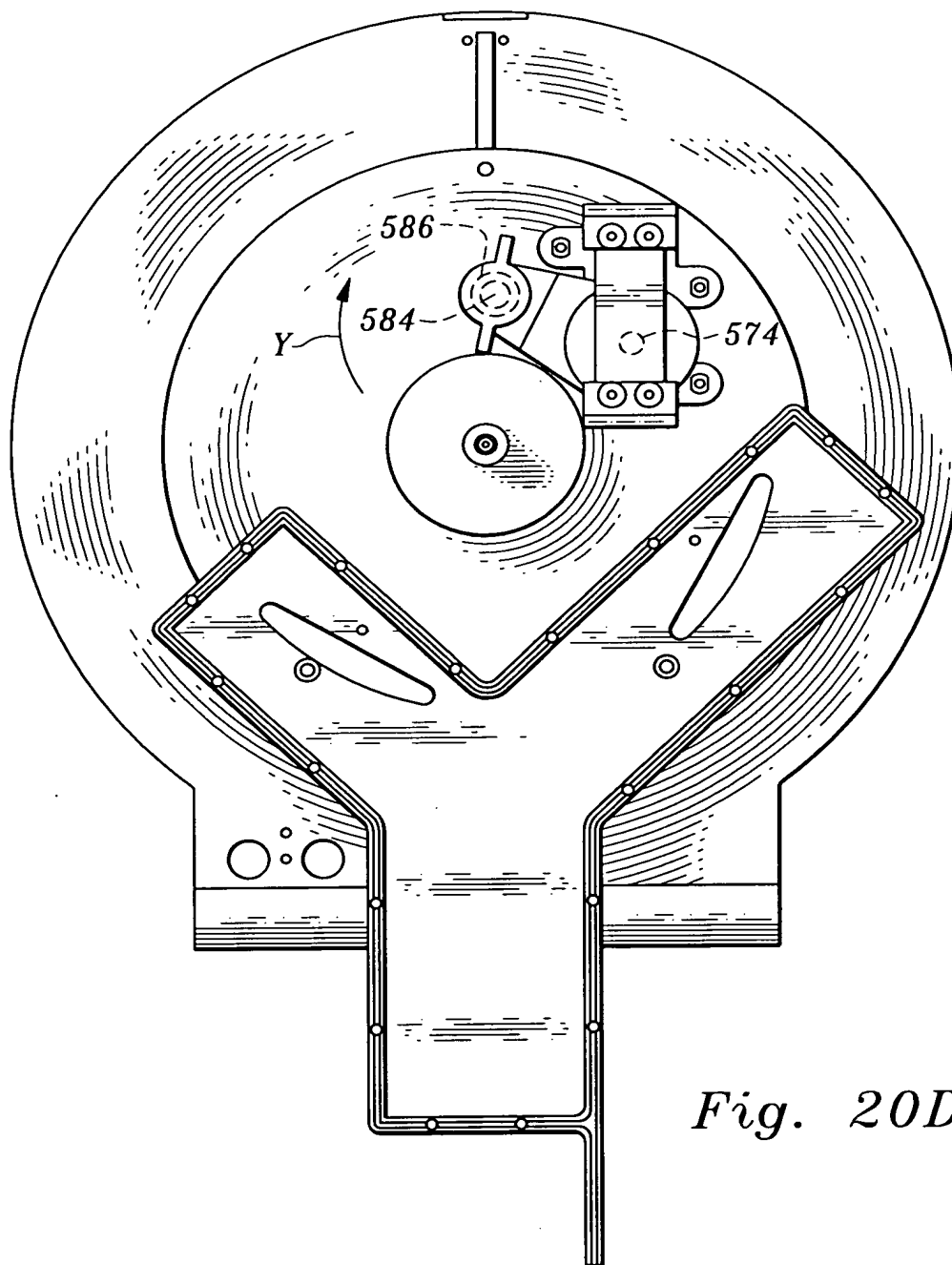


Fig. 20C



*Fig. 21*

<i>Fig. 21A</i>	<i>Fig. 21C</i>
<i>Fig. 21B</i>	<i>Fig. 21D</i>

Fig. 21B

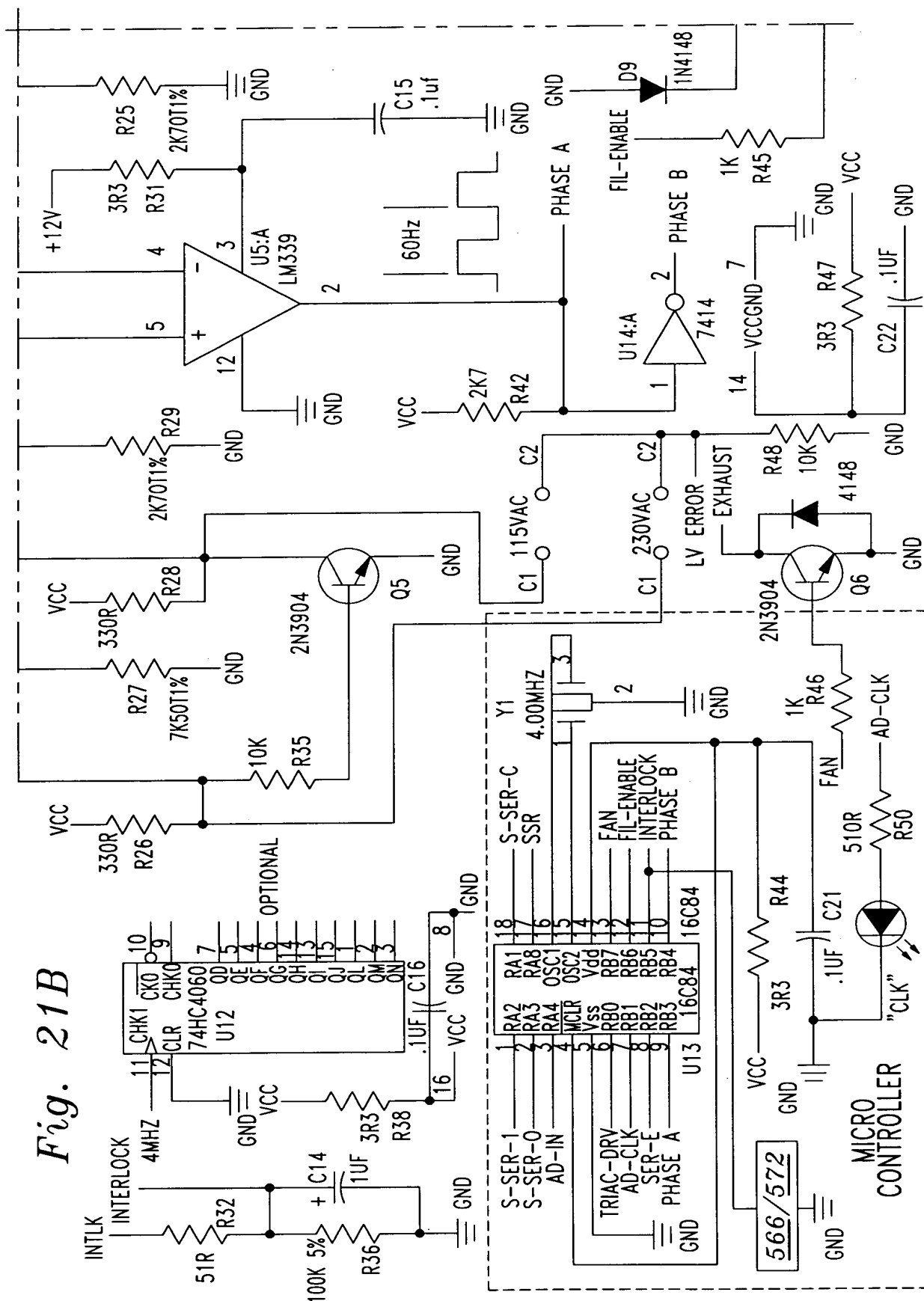




Fig. 21A

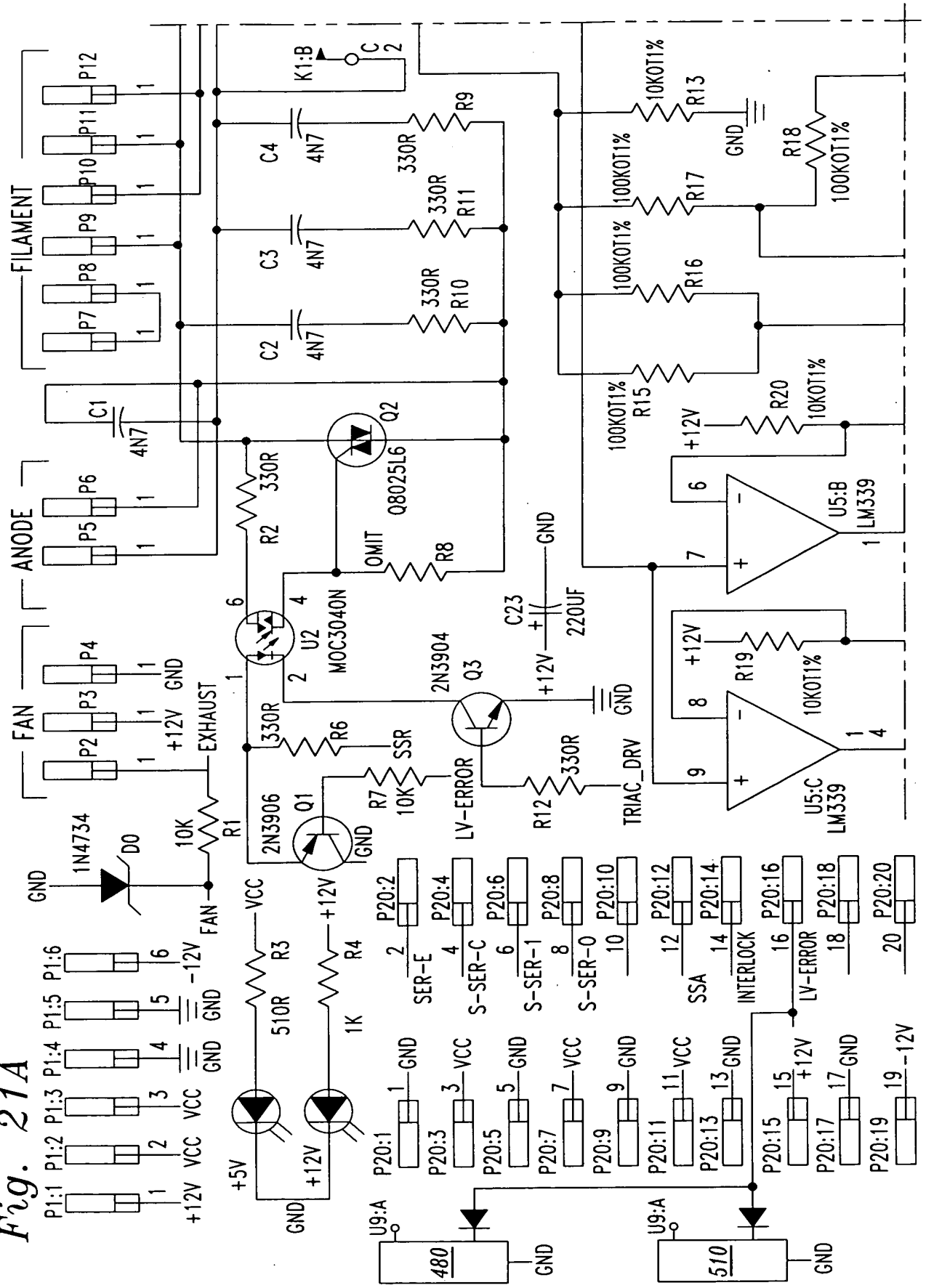
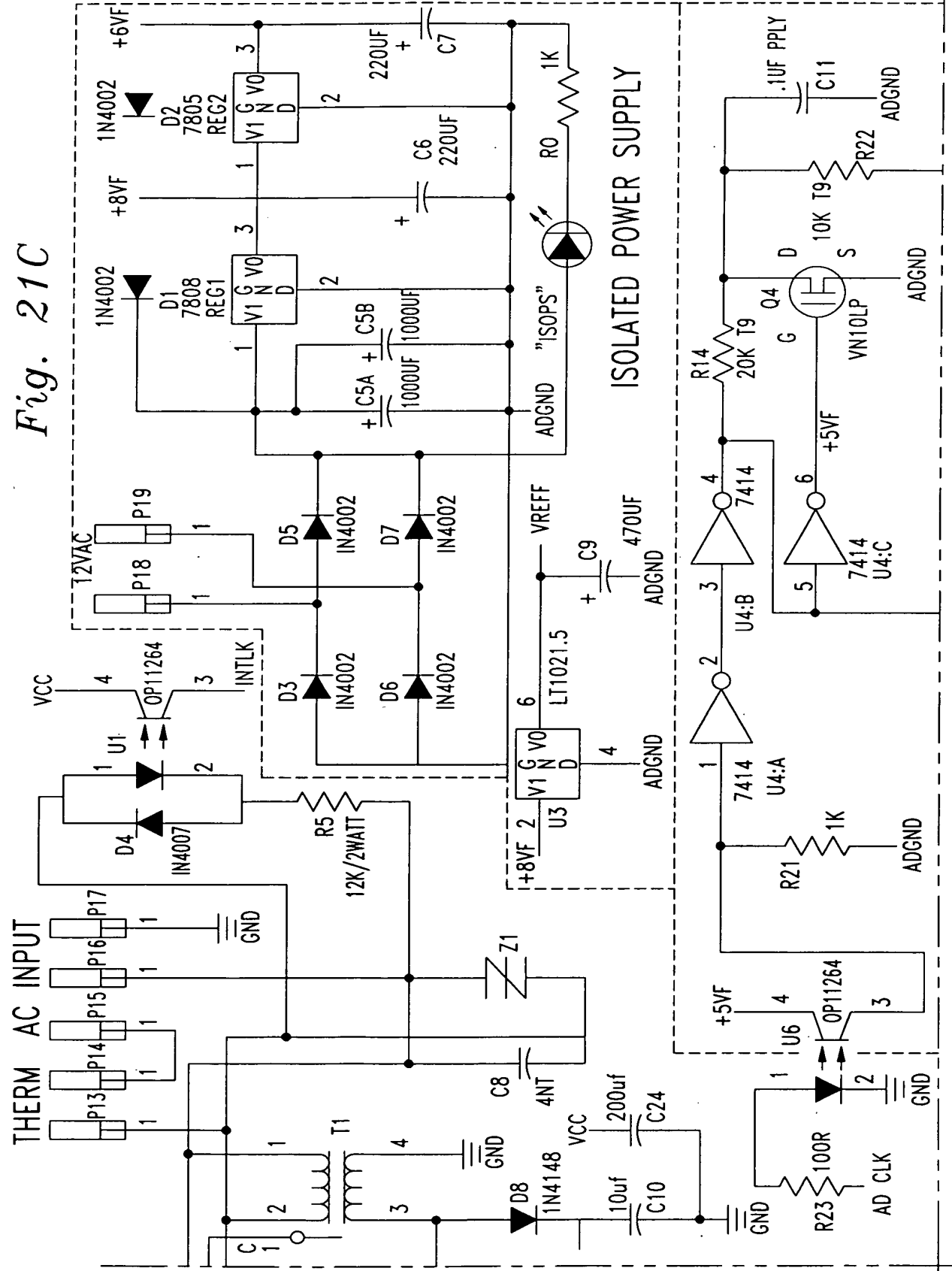
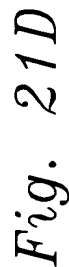


Fig. 21C





A/D CONVERTER

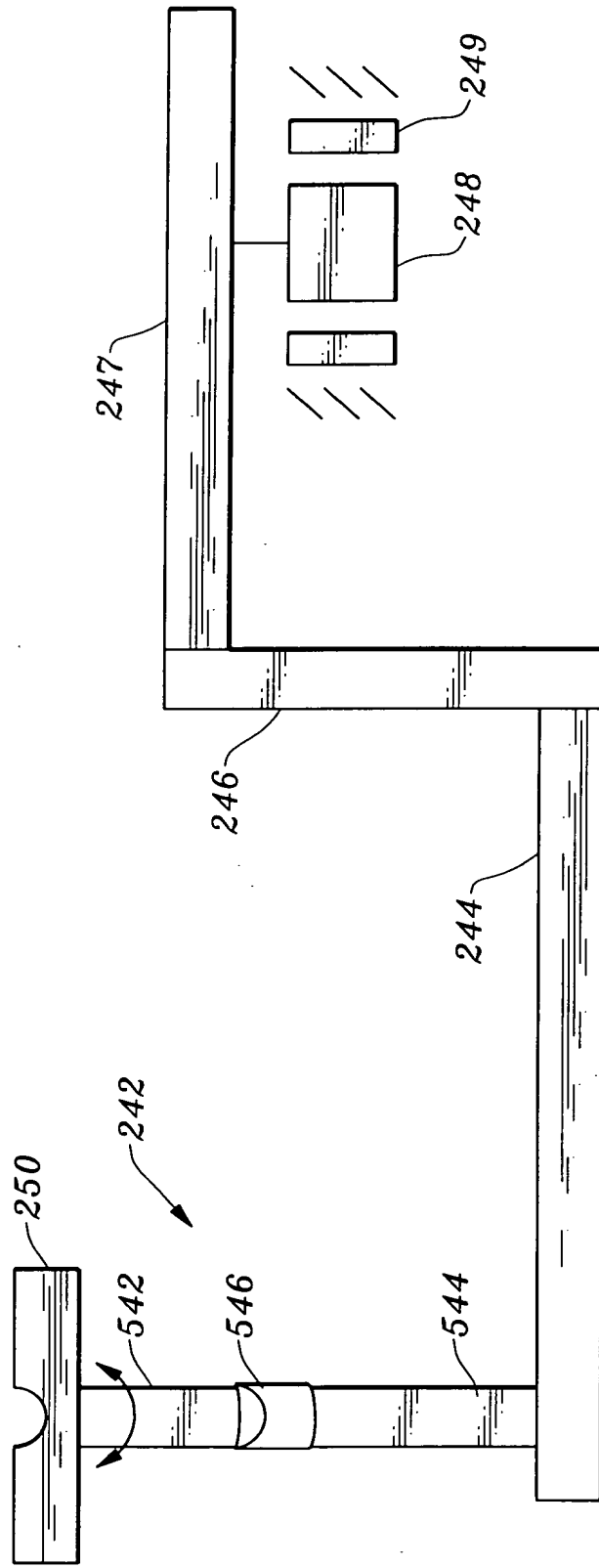


Fig. 22

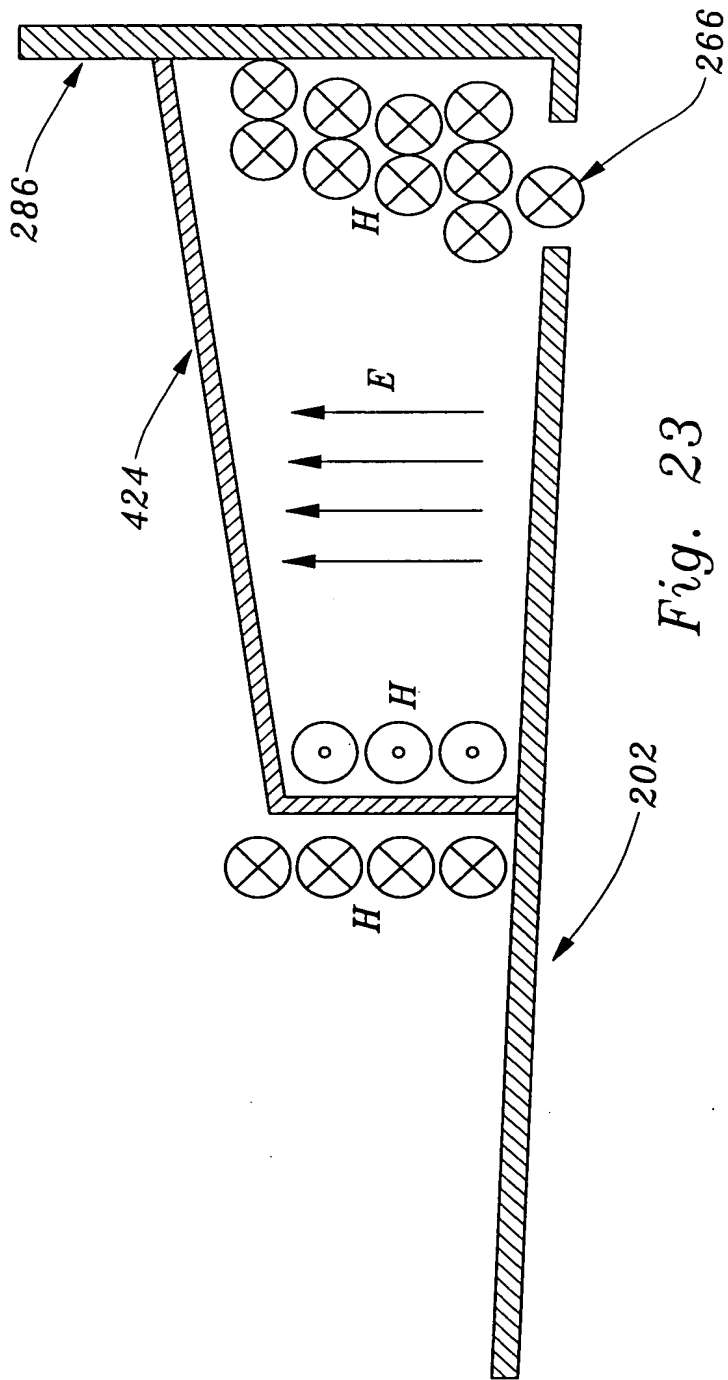


Fig. 23

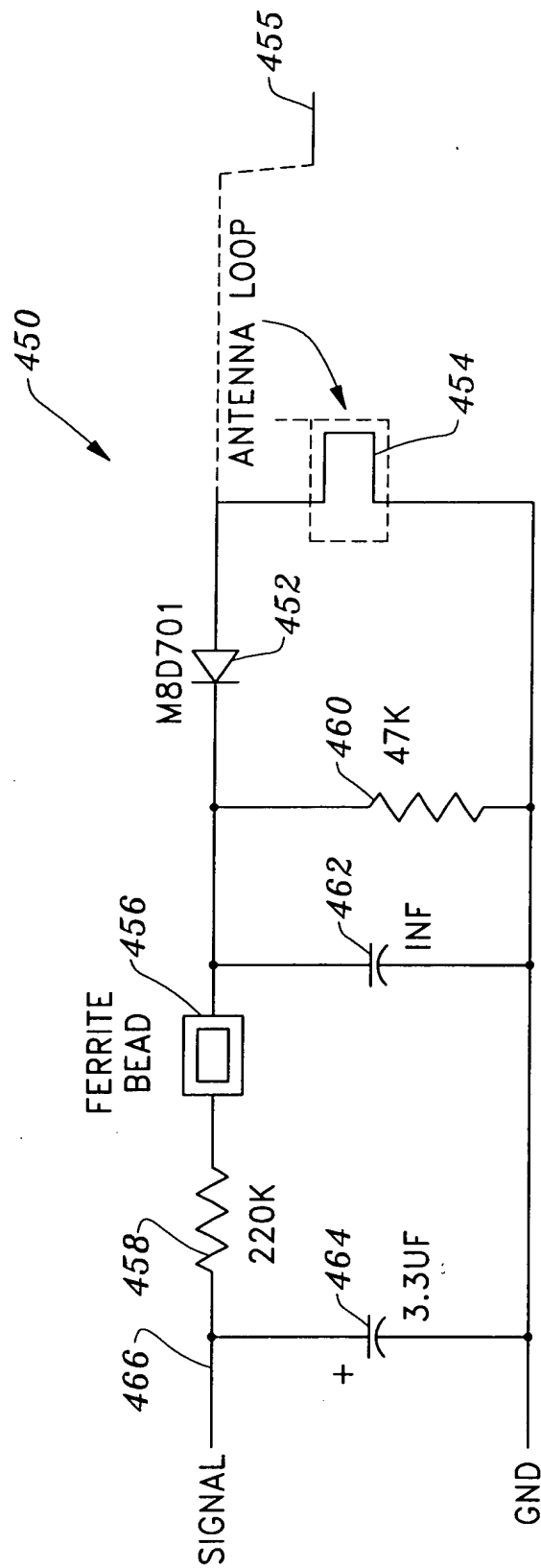


Fig. 24

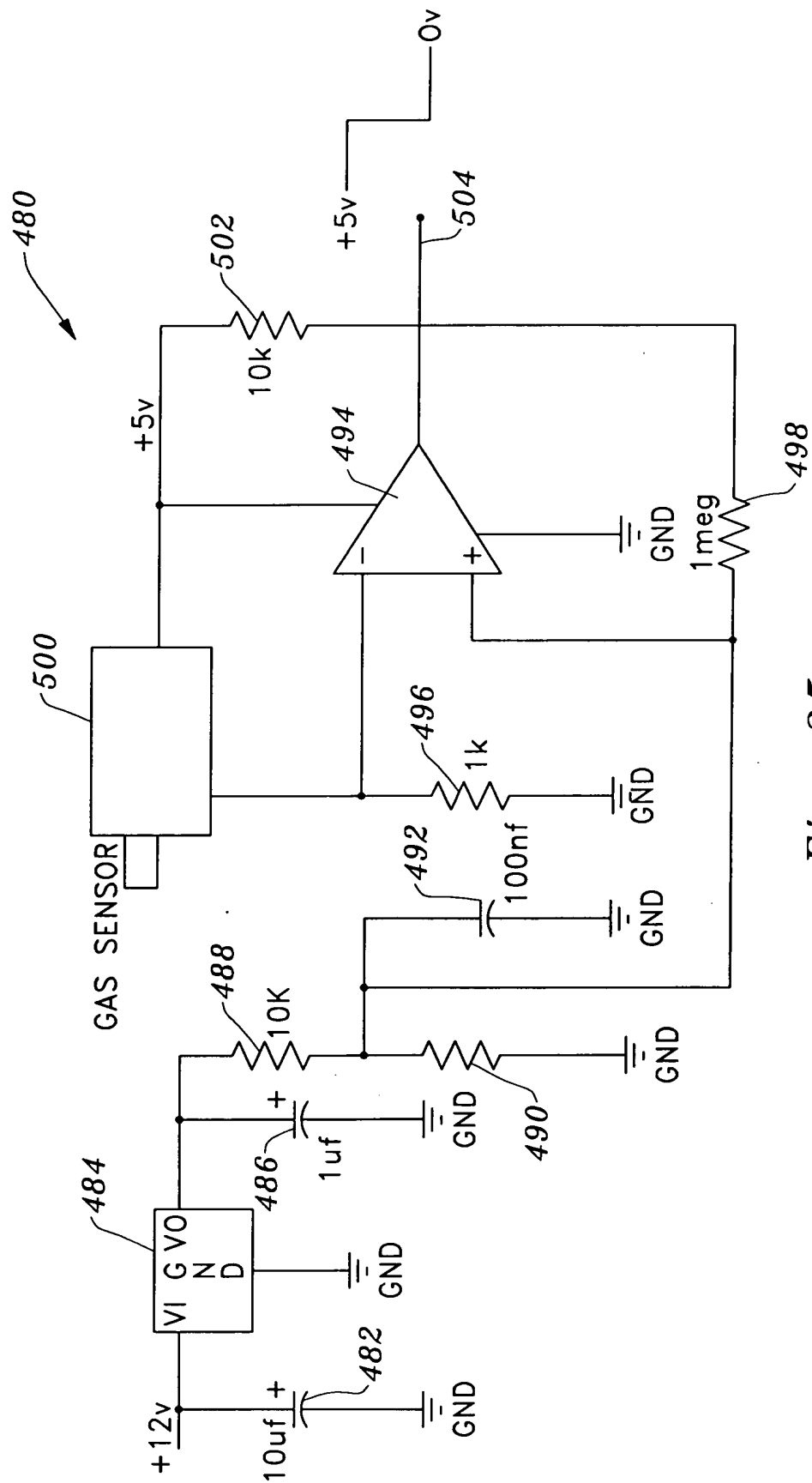


Fig. 25



Fig. 26



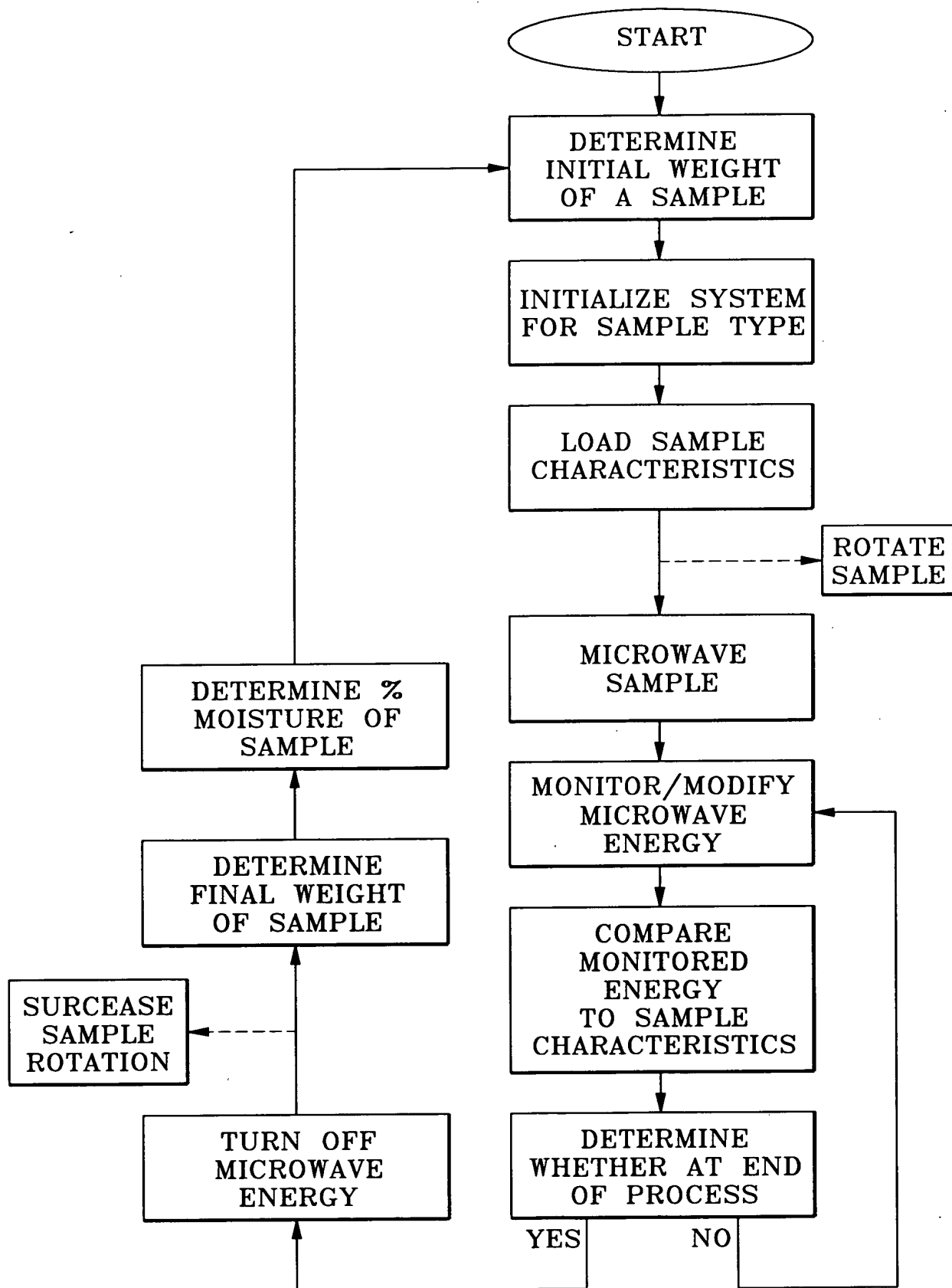


Fig. 27

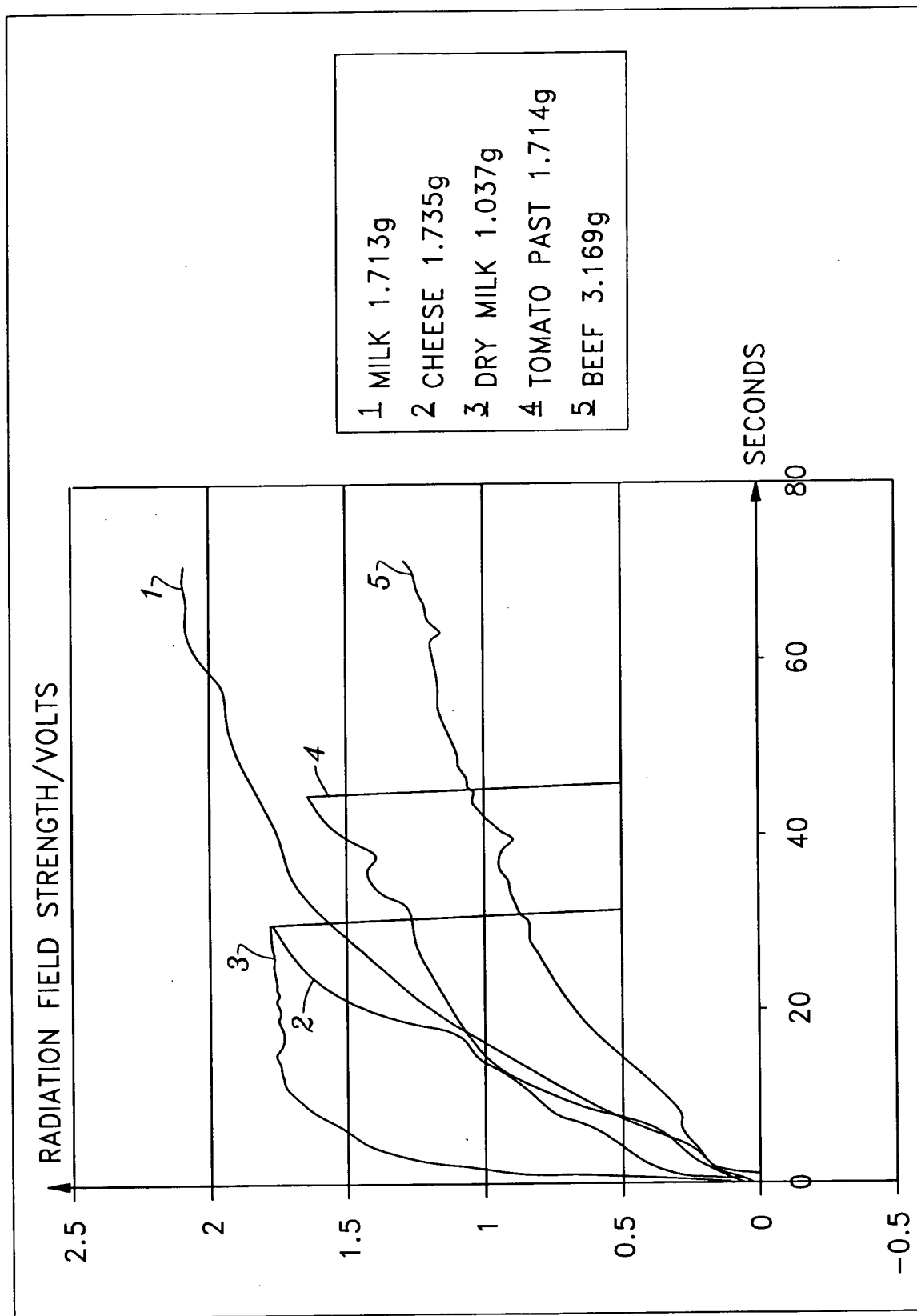


Fig. 28

